

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Art Unit : 2814  
Examiner: Mr. Phat X. Cao

DAC#  
# 5/a<sup>EX</sup>  
8/20/01  
Surles

In re PATENT APPLICATION of

Applicants : Yasutaka SAKAINO, et al.

Appln. No.: 09/229,628

Filed : January 13, 1999

For : SEMICONDUCTOR INTEGRATED  
CIRCUIT

Att'y Dkt. : 32014-141666 (Formerly OKI 4646.01)

AMENDMENT

February 13, 2001

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

PETITION FOR EXTENSION OF TIME

Please extend the period for responding to the Office Action of September 13, 2000 by

12/06/2001 SSURLES 00000008 220261 09229628

01 FC:102 80.00 CH  
02 FC:103 18.00 CH

two months, and thus until February 13, 2001. The requisite extension fee of \$390 is included in a

remittance that is being submitted concurrently. Should this remittance be inadvertently missing,

however, or should any additional fees be required (including additional extension-of-time fees,

since Applicants hereby provisionally petition for any extensions that may be deemed necessary to

avoid abandonment), the Commissioner may charge such fees to Deposit Account number

• 22-0261, and notify the undersigned attorney accordingly.

02/14/2001 CCHAU1 00000053 09229628

01 FC:116 390.00 DP

02/14/2001 CCHAU1 00000053 09229628

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